



IEEE Radio and Wireless Symposium  
San Diego, CA Jan 09

CLASS E RF/MICROWAVE POWER AMPLIFIERS

## Class E broadband amplifier with C-LC shunt network

Basic theory, simulation and prototype

A. Mediano<sup>1</sup>, K. Narendra<sup>2</sup>, C. Prakash<sup>2</sup>,

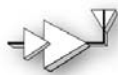


<sup>1</sup>13A, University of Zaragoza, Zaragoza, Spain

<sup>2</sup>Motorola Technology, Penang, Malaysia



MOTOROLA



RWW 2009

IEEE Topical Symposium on Power Amplifiers for Wireless  
Communications



IEEE Radio and Wireless Symposium  
San Diego, CA Jan09

2009 IEEE Topical Symposium on Power  
Amplifiers for Wireless Communications



## Motivation: Class E and broadband

- ❑ **Class E amps very interesting in modern communication systems ...**
  - Large number of channels for a long period from a small-size battery.
  - Power amplifier is a main consumer of dc power from a battery.
  - Class E offers high operating efficiency near 100 % for ideal case.
- ❑ **Class E amplifiers are narrowband by concept.**
  - Output network tuned to one frequency.
  - Nominal operation if switch "see" a nominal impedance at  $f_0$  and harmonics.
  - Published results for broadband operation: Grebennikov, Raab, Al-Shahrani, Gudimetla, Qin, Quach, Tat Hung, Tayrani, Everard, Pajic, etc.
- ❑ **Design of a broadband class E UHF amplifier ...**
  - With choke in drain to supply,
  - With C parallel with device (or more) included in output network

**Bandwidth:** 403-470MHz

**Rated power:** 4W

**Load:** 50 $\Omega$

**Efficiency:** 75% @  $P_{out}=5W$

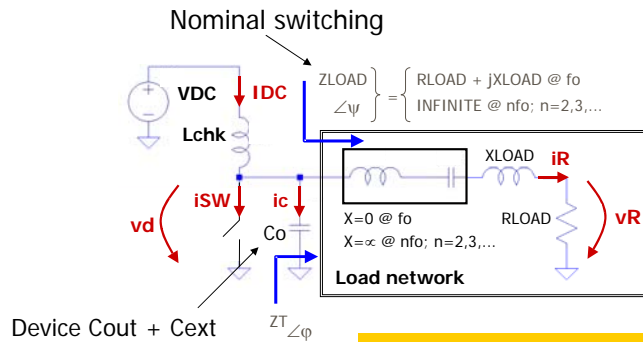
**Power supply:** 7.5V

**Harmonic spectrum:** Additional filter for harmonic rejection.

RWW 09 · A. Mediano · amediano@unizar.es



## Class E: basics of the idea

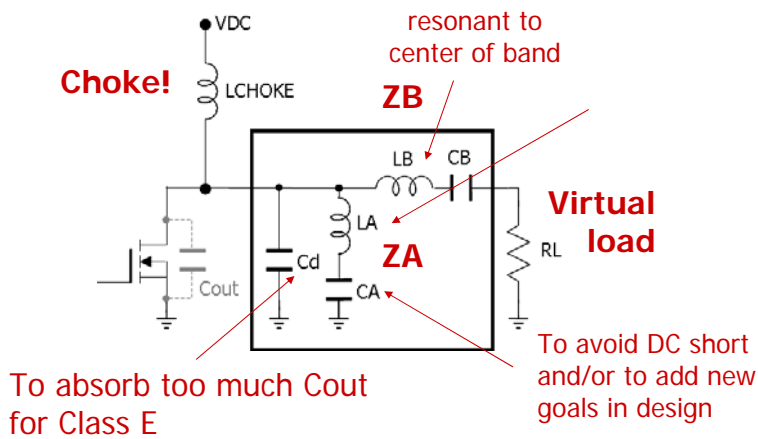


Try a new output network designed to show almost  $Z_{LOAD}$  for the complete broadband, 400-470MHz.

RWW 09 · A. Mediano · amediano@unizar.es



## Broadband: output network



RWW 09 · A. Mediano · amediano@unizar.es

Analysis was done with similar method as Raab'76.



## Broadband: equations for the design (1)

Considering: i) the ZLOAD to show to device at fo; ii) the angle of ZLOAD to show to device (49°); iii) the slope of that angle =0 for broadband design; iv) the resonant frequency of LB-CB must be fo and v) the CA value must be a low impedance versus LA impedance.

$$LBx(\omega x, \psi x, Cx, Cdx, Nratiox) := \frac{8.0 \cdot 10^{-4}}{\omega x^2 Cx^2 (Nratiox - 1.)} \left[ \begin{array}{l} 175. (\tan(\psi x)^2 + 1)^{\frac{1}{2}} Cx \tan(\psi x) + \dots \\ + 175. (\tan(\psi x)^2 + 1)^{\frac{1}{2}} Cx Nratiox \tan(\psi x) + \dots \\ + 98. Cdx Nratiox + 98. Cdx Nratiox \tan(\psi x)^2 \end{array} \right]$$

$$CBx(\omega x, \psi x, Cx, Cdx, Nratiox) := 1250. Cx^2 \frac{Nratiox - 1.}{175. (\tan(\psi x)^2 + 1)^{\frac{1}{2}} Cx \tan(\psi x) + \dots + 175. (\tan(\psi x)^2 + 1)^{\frac{1}{2}} Cx Nratiox \tan(\psi x) + \dots + 98. Cdx Nratiox + 98. Cdx Nratiox \tan(\psi x)^2}$$

RWW 09 · A. Mediano · amediano@unizar.es

Analysis was done with similar method as Raab'76.



## Broadband: equations for the design (2)

$$LAX(\omega x, \psi x, Cx, Cdx, Nratiox) := 7 Nratiox \frac{(\tan(\psi x)^2 + 1)^{\frac{1}{2}}}{\omega x^2 \left[ \begin{array}{l} 25. \tan(\psi x) Nratiox Cx - 25. \tan(\psi x) Cx + \dots \\ + 7 Cdx Nratiox (\tan(\psi x)^2 + 1)^{\frac{1}{2}} - 7. Cdx (\tan(\psi x)^2 + 1)^{\frac{1}{2}} \end{array} \right]}$$

$$RX(\omega x, \psi x, Cx, Cdx, Nratiox) := .28 \frac{(\tan(\psi x)^2 + 1)^{\frac{1}{2}}}{Cx \omega x} \left[ \begin{array}{l} 49. Cdx \tan(\psi x)^2 + 49. Cdx + \dots \\ + 175. (\tan(\psi x)^2 + 1)^{\frac{1}{2}} Cx \tan(\psi x) \end{array} \right]$$

$$CAX(\omega x, \psi x, Cx, Cdx, Nratiox) := (2 \cdot 10^{-2} Nratiox - 2 \cdot 10^{-2}) + \frac{175. (\tan(\psi x)^2 + 1)^{\frac{1}{2}} Cx \tan(\psi x)}{\tan(\psi x)^2 + 1.}$$

RWW 09 · A. Mediano · amediano@unizar.es

Analysis was done with similar method as Raab'76.



## The design: using the equations ...

Transistor RD07MVS1(Mitsubishi): Nominal  $R_{on}=0.18\Omega$   $C_{out}=60pF$

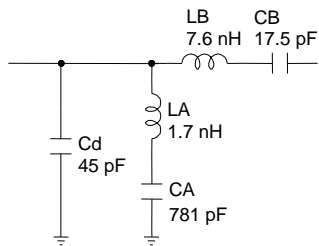
From classical narrowband class E theory:

$$C_o = 16.8pF @ 403MHz, \quad 15.5pF @ f_o ; \quad 14.4pF @ 470MHz$$

Using equations:

$$C_x := 15.5 \cdot pF \quad f_x := 436.5 \cdot MHz \quad \psi_x := 49 \cdot deg$$

$$\omega_x := 2 \cdot \pi \cdot f_x \quad C_{dx} := 45 \cdot pF \quad N_{ratiox} := 10$$



$$LBx(\omega_x, \psi_x, C_x, C_{dx}, N_{ratiox}) = 7.613 nH$$

$$CBx(\omega_x, \psi_x, C_x, C_{dx}, N_{ratiox}) = 17.462 pF$$

$$LAx(\omega_x, \psi_x, C_x, C_{dx}, N_{ratiox}) = 1.702 nH$$

$$Rx(\omega_x, \psi_x, C_x, C_{dx}, N_{ratiox}) = 10.04 \Omega$$

$$CAx(\omega_x, \psi_x, C_x, C_{dx}, N_{ratiox}) = 781.007 pF$$

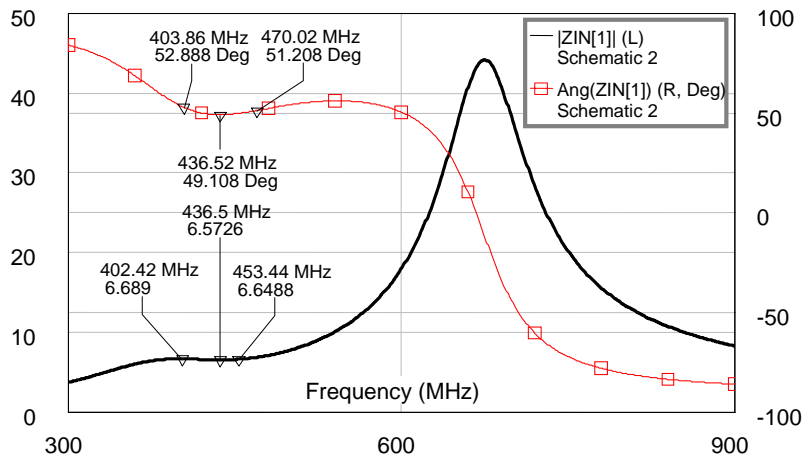
$$Z_{LOAD}(f_x, C_x) = 4.321 + 4.971j \Omega$$

$$|Z_{LOAD}(f_x, C_x)| = 6.587 \Omega$$

RWW 09 · A. Mediano · amediano@unizar.es



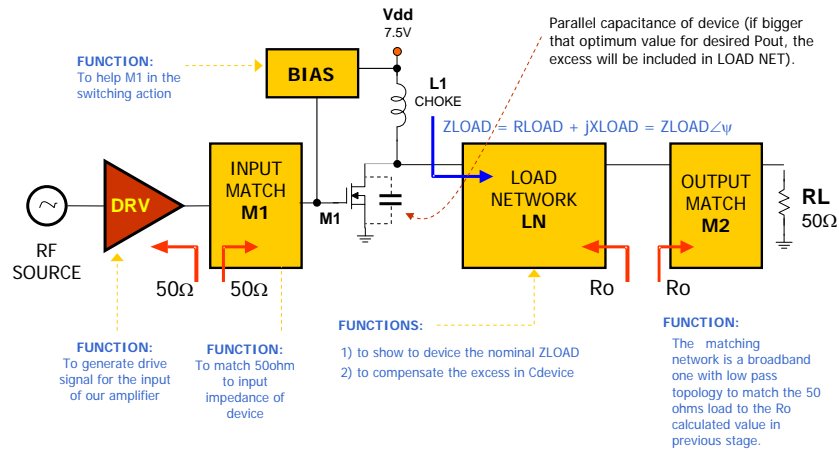
## The design: output network simulation



RWW 09 · A. Mediano · amediano@unizar.es



## The design: block diagram

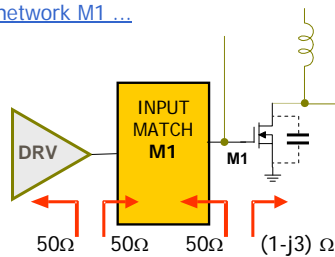


RWW 09 · A. Mediano · amediano@unizar.es



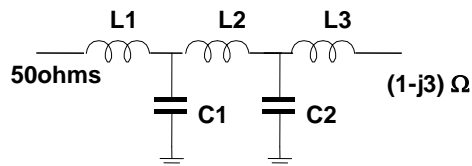
## The design: matching driver

About the input matching network M1 ...



- $Z_{IN} = 1\Omega -j3\Omega$  @ 430MHz [123pF]
- Broadband
- Lowpass response.
- Maximum saturated gain in PA

Topology ...



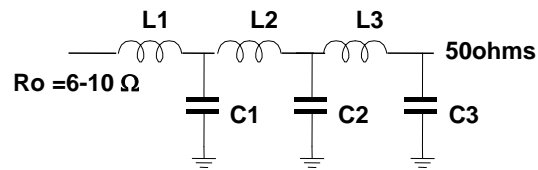
RWW 09 · A. Mediano · amediano@unizar.es



## The design: output matching

Simple (low parts count) and ladder L topology for broadband.

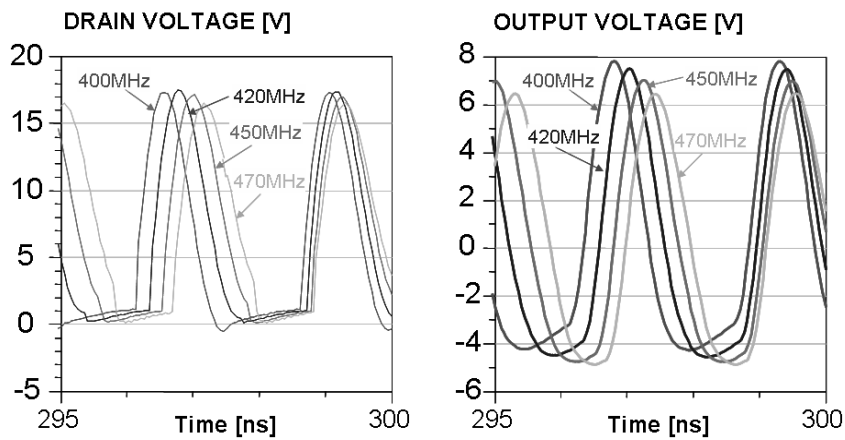
Theoretical design ...



RWW 09 · A. Mediano · amediano@unizar.es



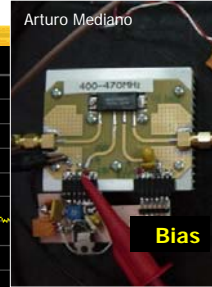
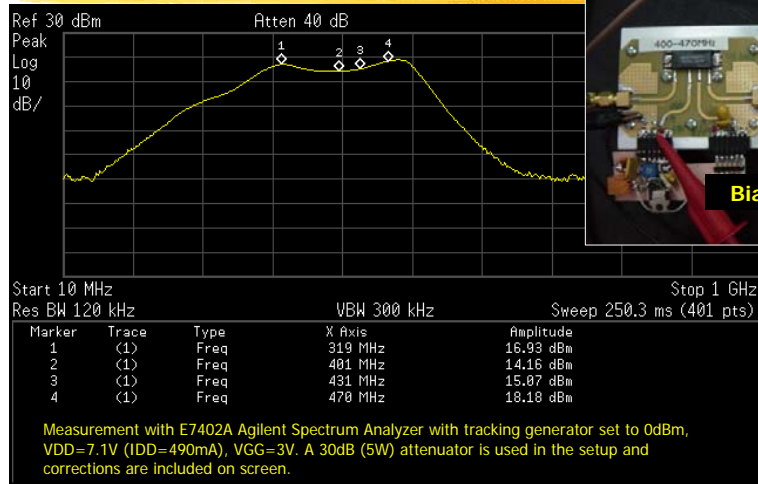
## The design: simulation (time domain)



RWW 09 · A. Mediano · amediano@unizar.es



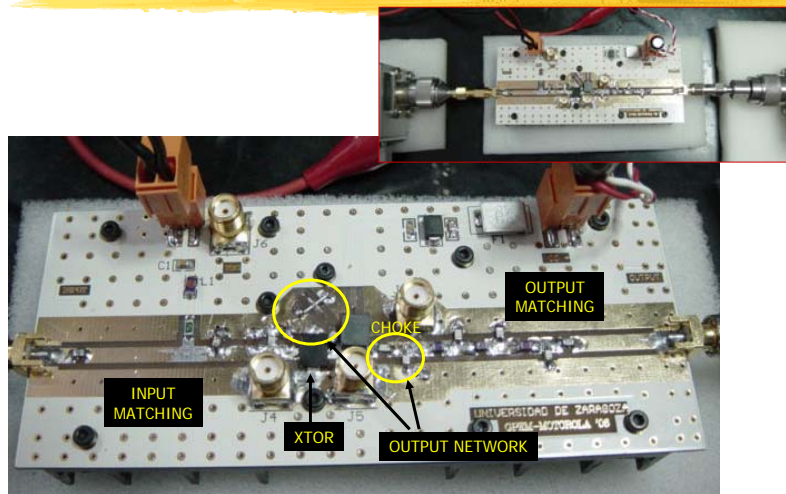
## Prototype: the driver



RWW 09 · A. Mediano · amediano@unizar.es



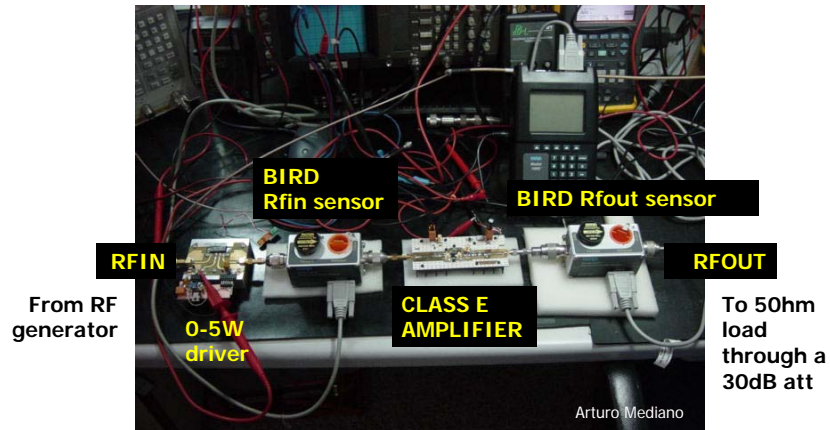
## Prototype: the class E PCB



RWW 09 · A. Mediano · amediano@unizar.es



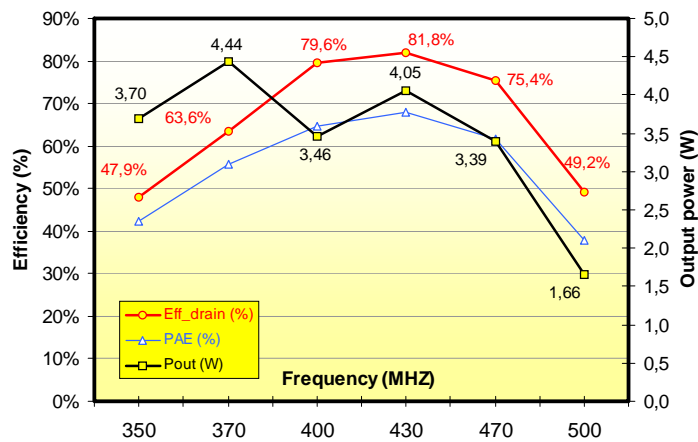
## Prototype: characterization setup



RWW 09 · A. Mediano · amediano@unizar.es



## Prototype: measurements



RWW 09 · A. Mediano · amediano@unizar.es





## Conclusions

- ⌘ **Analytical expressions** for a class E design with a **C-LC output network** for broadband, with choke in drain and excess in device output capacitance compensation-absortion were shown.
- ⌘ An UHF amplifier was **designed, simulated and optimized** with real world components and prototype parasitics with good results.
- ⌘ A big transistor with excess in Cout was used for the design.
- ⌘ **A prototype has been built** to work in broadband.
- ⌘ Main problems were found in the model of transistor as a switch and the output inductance of device.
- ⌘ This exploration has lead to the conclusion that the C-LC series output branch in the Class E amplifier is an attractive solution for radios where operation over a large number of channels for a long period and from a small size battery is typical.

RWW 09 · A. Mediano · amediano@unizar.es



## Thanks for your attention...

Dr. Arturo MEDIANO  
[a.mediano@ieee.org](mailto:a.mediano@ieee.org)  
[www.cartoontronics.com](http://www.cartoontronics.com)



RWW 09 · A. Mediano · amediano@unizar.es